

Question # 1 of 30 (Start time: 07:50:28 PM, 27 December 2020)

There are _____ types of address wraparound.

Select the correct option



1



2



3



4

Question # 3 of 30 (Start time: 07:53:31 PM, 27 December 2020)

The swap flag can be stored in _____.

Select the correct option



a register



memory



both register and memory



neither register nor memory

Question # 4 of 30 (Start time: 07:54:14 PM, 27 December 2020)

Flags register is a special register in every architecture. It is also known as _____.

Select the correct option



Carry Flag



accumulator register



Program Status Word



special register

Question # 5 of 30 (Start time: 07:55:07 PM, 27 December 2020)

REPE and REPNE prefixes are only meaningful with _____.

Select the correct option



MOV



LDS



CMPS



STOS

Question # 6 of 30 (Start time: 07:55:49 PM, 27 December 2020)

Group of bits processor uses to inform memory which element to read/write is collectively known as

Select the correct option



Control bus



Data bus



Address bus



RAM

Question # 7 of 30 (Start time: 07:56:43 PM, 27 December 2020)

The mechanism used to drop carry for making the calculated address valid is known as:

Select the correct option



Carry Overload



Overflow



Address Wraparound



Segment Overlapping

Question # 8 of 30 (Start time: 07:58:11 PM, 27 December 2020)

During program execution, if any change occurs in AH or AL, is also reflected in _____.

Select the correct option



AL



AX



AH



DX

Question # 9 of 30 (Start time: 07:58:43 PM, 27 December 2020)

CS and IP are both _____ bit registers.

Select the correct option



8



4



16



32

Question # 10 of 30 (Start time: 07:59:53 PM, 27 December 2020)

In general, width of a memory cell cannot be greater than the width of _____.

Select the correct option



Control Bus



Data Bus



Address Bus



I/O Bus

Question # 11 of 30 (Start time: 08:01:38 PM, 27 December 2020)

There are _____ block processing instructions in 8088.

Select the correct option



6



5



4



3

Question # 12 of 30 (Start time: 08:02:50 PM, 27 December 2020)

Which of the following is a special instruction?

Select the correct option



cmp ax, 0



add bx, 0534



mov ax, bx



cli

Question # 13 of 30 (Start time: 08:03:30 PM, 27 December 2020)

In case of short jump, the offset is stored in _____ .

Select the correct option



1 byte



2 bytes



4 bytes



16 bytes

Question # 14 of 30 (Start time: 08:04:08 PM, 27 December 2020)

90 is the op-code of

Select the correct option



Do nothing



Add



Subtract



Multiplication

Question # 15 of 30 (Start time: 08:05:32 PM, 27 December 2020)

Extended Shifting Algorithm consists of:

Select the correct option



1 instruction



2 instructions



3 instructions



4 instructions

Question # 16 of 30 (Start time: 08:06:45 PM, 27 December 2020)

CX register is mostly used as a _____ register.

Select the correct option



counter



flag



base



destination

Question # 17 of 30 (Start time: 08:07:34 PM, 27 December 2020)

In MUL instruction, if the source operand is a byte, then the result is returned in _____ and _____.

Select the correct option



AH, AX



AL, AX



AH, AL



BH, BL

Question # 18 of 30 (Start time: 08:08:46 PM, 27 December 2020)

In MUL instruction, if the source operand is a byte, then it is multiplied with _____ register.

Select the correct option



AH



AL



BH



BL

Question # 19 of 30 (Start time: 08:09:32 PM, 27 December 2020)

An important role of the stack is in the creation of _____ variables that are only needed while the subroutine is in execution and not afterwards.

Select the correct option



Global



Local



Private



Public

Question # 20 of 30 (Start time: 08:10:30 PM, 27 December 2020)

The basic function of register is to hold _____.

Select the correct option



operands



operators



mnemonics



opcodes

Question # 21 of 30 (Start time: 08:11:59 PM, 27 December 2020)

In 8051, there is an _____ stack.

Select the correct option



incrementing



stagnant



decrementing



semi-automatic

Question # 21 of 30 (Start time: 08:11:59 PM, 27 December 2020)

In 8051, there is an _____ stack.

Select the correct option



incrementing



stagnant



decrementing



semi-automatic

Question # 22 of 30 (Start time: 08:13:02 PM, 27 December 2020)

When the operand of DIV instruction is in 16-bits, then the implied dividend will be in _____.

Select the correct option



AX



DX:AX



ES:AX



DS:BX

Question # 23 of 30 (Start time: 08:13:41 PM, 27 December 2020)

Code size reduction and improvement in speed were the two reasons for introducing block processing instructions in the _____ processor.

Select the correct option



8088



8085



8080



iAPX386

Question # 24 of 30 (Start time: 08:14:48 PM, 27 December 2020)

The clear screen operation initializes the whole block of video memory to _____.

Select the correct option



0x0721



0x0714



0x0720



0x0175

Question # 25 of 30 (Start time: 08:15:57 PM, 27 December 2020)

To convert the case of a character, we add or subtract _____ from its ASCII code.

Select the correct option



0x10



0x20



0x30



0x41

Question # 26 of 30 (Start time: 08:16:52 PM, 27 December 2020)

By default, BP is associated with _____.

Select the correct option



CS



IP



SS



SP

Question # 27 of 30 (Start time: 08:18:43 PM, 27 December 2020)

The simplest form of addressing, in which the operand value is present in instruction is

Select the correct option



direct addressing



indirect addressing



immediate addressing



register indirect addressing

Question # 28 of 30 (Start time: 08:19:54 PM, 27 December 2020)

The most convenient place to store local variables is _____.

Select the correct option



main memory



registers



cache memory



stack

Question # 29 of 30 (Start time: 08:20:47 PM, 27 December 2020)

Stack is a data structure that behaves in _____ manner.

Select the correct option



LIFO



FIFO



Both LIFO and FIFO



LILO

Question # 30 of 30 (Start time: 08:21:46 PM, 27 December 2020)

_____ is a special instruction that loads a general purpose register and a segment register from two consecutive memory locations.

Select the correct option



SCAS



MOVS



CALL



LES



Click to Save Answer & Move to N