

# VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future  
CORRECT ANSWER SOLVED BY HADI  
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**TEAM HADI(VU LAHORE)**

## MID TERM PAST PAPER

We Deal All Kind of VU Project  
(PHP, .NET, ANDROID, PYTHON)

**MADE AND SOLVED BY HADI**

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question # 2 of 30 ( Start time: 10:23:55 PM, 28 December 2020 )

Total Marks: 1

\_\_\_\_\_ have Buffers at the inputs which produce the actual variable and its complement.

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Select the correct option

|                                  |       |
|----------------------------------|-------|
| <input type="radio"/>            | EPROM |
| <input checked="" type="radio"/> | PAL   |
| <input type="radio"/>            | PLA   |
| <input type="radio"/>            | GAL   |

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## Question # 1 of 30 ( Start time: 02:34:57 PM, 28 December 2020 )

Total Marks: 1

In iterative circuit implementation of  $A=B$  function, If input  $A_0$  is equal to input  $B_0$ , the \_\_\_\_ gate output in Module 0 is a 1 which is passed on to Module 1 through its Cascading input.

Select the correct option

|                                  |      |  |
|----------------------------------|------|--|
| <input type="radio"/>            | OR   |  |
| <input type="radio"/>            | XOR  |  |
| <input type="radio"/>            | NOR  |  |
| <input checked="" type="radio"/> | XNOR |  |

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Question # 2 of 30 ( Start time: 02:35:59 PM, 28 December 2020 )

Total Marks: 1

\_\_\_\_\_ is invalid number of cells in a single group formed by the adjacent cells in K-map

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Select the correct option

|                                  |    |  |
|----------------------------------|----|--|
| <input type="radio"/>            | 2  |  |
| <input type="radio"/>            | 8  |  |
| <input checked="" type="radio"/> | 12 |  |
| <input type="radio"/>            | 16 |  |

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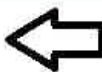
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Question # 28 of 30 ( Start time: 02:59:13 PM, 28 December 2020 )

Total Marks: 1

The \_\_\_\_\_ is the slowest and consumes more power.

Select the correct option

- |                                  |                        |                                                                                   |
|----------------------------------|------------------------|-----------------------------------------------------------------------------------|
| <input checked="" type="radio"/> | Standard TTL           |  |
| <input type="radio"/>            | Schottky TTL           |                                                                                   |
| <input type="radio"/>            | Advanced Schottky TTL  |                                                                                   |
| <input type="radio"/>            | Low-Power Schottky TTL |                                                                                   |

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Question # 24 of 30 ( Start time: 02:56:00 PM, 28 December 2020 )

Total Marks: 1

8-bit parallel data can be converted into serial data by using \_\_\_\_\_ multiplexer

Select the correct option

|                                  |        |   |
|----------------------------------|--------|---|
| <input type="radio"/>            | 4-to-2 |   |
| <input type="radio"/>            | 4-to-4 |   |
| <input checked="" type="radio"/> | 8-to-1 | ← |
| <input type="radio"/>            | 8-to-4 |   |

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Question # 9 of 30 ( Start time: 10:29:28 PM, 28 December 2020 )

Total Marks

The measurable values generally change over a \_\_\_\_\_.

Select the correct option

|                                  |                  |  |
|----------------------------------|------------------|--|
| <input type="radio"/>            | Specific period  |  |
| <input type="radio"/>            | Discrete range   |  |
| <input type="radio"/>            | Time             |  |
| <input checked="" type="radio"/> | Continuous range |  |

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Question # 29 of 30 ( Start time: 10:43:53 PM, 28 December 2020 )

Total Marks

The Quine-McCluskey method has \_\_\_\_\_ number of steps.

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Select the correct option

☒

2

☐

3

☐

4

☐

5

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Question # 8 of 30 ( Start time: 10:28:36 PM, 28 December 2020 )

Total Marks: 1

For a Standard SOP expression, a \_\_\_\_\_ is placed in the cell corresponding to the product term (Minterm) present in the expression.

Select the correct option

|                                  |                                            |  |
|----------------------------------|--------------------------------------------|--|
| <input type="radio"/>            | 0                                          |  |
| <input checked="" type="radio"/> | 1                                          |  |
| <input type="radio"/>            | X (don't care condition)                   |  |
| <input type="radio"/>            | Any of given option depending on SOP term. |  |

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Question # 21 of 30 ( Start time: 10:39:19 PM, 28 December 2020 )

Total Marks: 1

A \_\_\_\_\_ can not operate without a memory element.

Select the correct option

|                                  |         |
|----------------------------------|---------|
| <input type="radio"/>            | cell    |
| <input checked="" type="radio"/> | counter |
| <input type="radio"/>            | clock   |
| <input type="radio"/>            | unit    |

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Question # 27 of 30 ( Start time: 10:42:58 PM, 28 December 2020 )

Total Marks:

The AND Gate performs a logical \_\_\_\_\_ function

Select the correct option

|                                  |                |
|----------------------------------|----------------|
| <input type="radio"/>            | Addition       |
| <input type="radio"/>            | Subtraction    |
| <input checked="" type="radio"/> | Multiplication |
| <input type="radio"/>            | Division       |

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Question # 19 of 30 ( Start time: 10:37:55 PM, 28 December 2020 )

Total Me

The ANSI/IEEE Standard 754 defines a \_\_\_\_\_ Single-Precision Floating Point format for binary numbers.

Select the correct option

|                                  |        |
|----------------------------------|--------|
| <input type="radio"/>            | 8-bit  |
| <input type="radio"/>            | 16-bit |
| <input checked="" type="radio"/> | 32-bit |
| <input type="radio"/>            | 64-bit |

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Question # 16 of 30 ( Start time: 10:35:56 PM, 28 December 2020 )

Total Marks: 1

\_\_\_\_\_ is invalid number of cells in a single group formed by the adjacent cells in K-map

Select the correct option

|                                  |    |  |
|----------------------------------|----|--|
| <input checked="" type="radio"/> | 2  |  |
| <input type="radio"/>            | 8  |  |
| <input type="radio"/>            | 12 |  |
| <input type="radio"/>            | 16 |  |

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Question # 4 of 30 ( Start time: 02:37:46 PM, 28 December 2020 )

Total Marks: 1

The below are Integrated Circuit Technologies except \_\_\_\_\_.

Select the correct option

|                                  |       |
|----------------------------------|-------|
| <input type="radio"/>            | TTL   |
| <input type="radio"/>            | ECL   |
| <input type="radio"/>            | PMOS  |
| <input checked="" type="radio"/> | ETMOS |

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Question # 9 of 30 ( Start time: 02:43:19 PM, 28 December 2020 )

Total Marks: 1

A \_\_\_\_\_ is converted into a standard POS by using the rule  $AA' = 0$ .

Select the correct option

[Reload Math Equations](#)

|                                  |                  |
|----------------------------------|------------------|
| <input type="radio"/>            | Non-standard SOP |
| <input checked="" type="radio"/> | Non-standard POS |
| <input type="radio"/>            | Simplified SOP   |
| <input type="radio"/>            | Simplified POS   |

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Question # 11 of 30 ( Start time: 02:44:46 PM, 28 December 2020 )

Total Marks: 1

Boolean algebra is the \_\_\_\_\_ of Digital Systems.

Select the correct option

|                                  |             |  |
|----------------------------------|-------------|--|
| <input type="radio"/>            | Science     |  |
| <input checked="" type="radio"/> | Mathematics |  |
| <input type="radio"/>            | Expression  |  |
| <input type="radio"/>            | Equation    |  |

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Question # 11 of 30 ( Start time: 10:30:31 PM, 28 December 2020 )

Total Marks: 1

In 16-input multiplexer, the decoder inputs \_\_\_\_ and \_\_\_\_ enable one out of the four multiplexers.

Select the correct option

- |                                  |      |
|----------------------------------|------|
| <input type="radio"/>            | A, B |
| <input type="radio"/>            | B, C |
| <input checked="" type="radio"/> | C, D |
| <input type="radio"/>            | A, D |

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Question # 28 of 30 ( Start time: 10:43:13 PM, 28 December 2020 )

Total Marks: 1

The Programmable Array Logic (PAL) has \_\_\_\_\_ AND array and a \_\_\_\_\_ OR array.

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Select the correct option

- |                                  |                            |
|----------------------------------|----------------------------|
| <input type="radio"/>            | Fixed, Programmable        |
| <input checked="" type="radio"/> | Programmable, Fixed        |
| <input type="radio"/>            | Fixed, Fixed               |
| <input type="radio"/>            | Programmable, Programmable |

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Question # 6 of 30 ( Start time: 10:26:53 PM, 28 December 2020 )

Total Mark

A variable can have a \_\_\_\_ value.

Select the correct option

|                                  |         |  |
|----------------------------------|---------|--|
| <input type="radio"/>            | 0       |  |
| <input type="radio"/>            | 1       |  |
| <input checked="" type="radio"/> | 0 or 1  |  |
| <input type="radio"/>            | 0 and 1 |  |

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Question # 25 of 30 ( Start time: 10:41:52 PM, 28 December 2020 )

Total Marks: 1

The OR Gate performs a Boolean \_\_\_\_\_ function

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Select the correct option

- |                                  |                |
|----------------------------------|----------------|
| <input checked="" type="radio"/> | Addition       |
| <input type="radio"/>            | Subtraction    |
| <input type="radio"/>            | Multiplication |
| <input type="radio"/>            | Division       |

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Question # 12 of 30 ( Start time: 10:31:09 PM, 28 December 2020 )

Total Marks: 1

The boolean expression  $X = AB + CD$  represents

Select the correct option

- |                                  |                        |  |
|----------------------------------|------------------------|--|
| <input type="radio"/>            | two ORs ANDed together |  |
| <input type="radio"/>            | a 4-input AND gate     |  |
| <input checked="" type="radio"/> | two ANDs ORed together |  |
| <input type="radio"/>            | an exclusive-Or        |  |

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Question # 15 of 30 ( Start time: 02:48:16 PM, 28 December 2020 )

Total Marks: 1

A 5-variable karnaugh map has

Select the correct option

|                                  |                  |  |
|----------------------------------|------------------|--|
| <input type="radio"/>            | Eight cells      |  |
| <input type="radio"/>            | Sixteen cells    |  |
| <input checked="" type="radio"/> | Thirty two cells |  |
| <input type="radio"/>            | Sixty four cells |  |

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Question # 18 of 30 ( Start time: 10:37:29 PM, 28 December 2020 )

Total Marks: 1

| Inputs |   | Output |
|--------|---|--------|
| A      | B | F      |
| 0      | 0 | 0      |
| 0      | 1 | 1      |
| 1      | 0 | 1      |
| 1      | 1 | 1      |

Select the correct option

|                                  |     |
|----------------------------------|-----|
| <input type="radio"/>            | AND |
| <input type="radio"/>            | NOT |
| <input checked="" type="radio"/> | OR  |
| <input type="radio"/>            | XOR |

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Question # 30 of 30 ( Start time: 10:45:03 PM, 28 December 2020 )

Total Marks: 1

In 16-bit ALU, the Carry Out 16 is generated only when ALU3 has received Carry In \_\_\_\_.

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Select the correct option

|                                  |    |
|----------------------------------|----|
| <input type="radio"/>            | 2  |
| <input type="radio"/>            | 4  |
| <input type="radio"/>            | 8  |
| <input checked="" type="radio"/> | 12 |

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Question # 3 of 30 ( Start time: 02:37:04 PM, 28 December 2020 )

Total Marks: 1

The GAL16V8 has eight \_\_\_\_\_ each connected to eight product terms.

Select the correct option

|                                  |       |
|----------------------------------|-------|
| <input checked="" type="radio"/> | OLMCs |
| <input type="radio"/>            | SOP   |
| <input type="radio"/>            | POS   |
| <input type="radio"/>            | cells |

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Question # 26 of 30 ( Start time: 02:57:25 PM, 28 December 2020 )

Total Marks: 1

A standard SOP form has \_\_\_\_\_ terms that have all the variables in the domain of the expression.

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Select the correct option

|                                  |           |
|----------------------------------|-----------|
| <input type="radio"/>            | Sum       |
| <input checked="" type="radio"/> | Product   |
| <input type="radio"/>            | Max       |
| <input type="radio"/>            | Composite |

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Question # 22 of 30 ( Start time: 02:54:38 PM, 28 December 2020 )

Total Marks: 1

Caveman number system is Base \_\_\_\_\_ number system

Select the correct option

|                                  |    |
|----------------------------------|----|
| <input type="radio"/>            | 2  |
| <input checked="" type="radio"/> | 5  |
| <input type="radio"/>            | 10 |
| <input type="radio"/>            | 16 |

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## Question # 26 of 30 ( Start time: 10:42:10 PM, 28 December 2020 )

Total Marks: 1

The OLMC of the GAL16V8 is similar to the OLMC of the \_\_\_\_\_ with some enhancements.

Select the correct option

☒ GAL22V10☐ GAL16V10☐ GAL14V10☐ GAL12V10

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Question # 22 of 30 ( Start time: 10:39:56 PM, 28 December 2020 )

Total Marks: 1

A \_\_\_\_\_ attached to the Select inputs of the Demultiplexer routes the incoming serial bits to successive outputs where each bit is stored.

Select the correct option



memory



counter



buffer



data

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Question # 6 of 30 ( Start time: 02:39:50 PM, 28 December 2020 )

Total Marks: 1

The Boolean expression  $(A + B' + C)$  is

Select the correct option

|                                  |                |
|----------------------------------|----------------|
| <input checked="" type="radio"/> | a sum term     |
| <input type="radio"/>            | a literal term |
| <input type="radio"/>            | a product term |
| <input type="radio"/>            | a minterm      |

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Question # 17 of 30 ( Start time: 02:49:31 PM, 28 December 2020 )

Total Marks: 1

Tri-State Buffer is a \_\_\_\_\_ gate with a control line that disconnects the output from the input.

Select the correct option

|                                  |      |
|----------------------------------|------|
| <input type="radio"/>            | OR   |
| <input type="radio"/>            | AND  |
| <input type="radio"/>            | NAND |
| <input checked="" type="radio"/> | NOT  |

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Question # 13 of 30 ( Start time: 02:46:36 PM, 28 December 2020 )

Total Marks: 1

In 8-input multiplexer, the two outputs are connected through a/an \_\_\_\_\_ gate.

Select the correct option

|                                  |     |
|----------------------------------|-----|
| <input type="radio"/>            | AND |
| <input checked="" type="radio"/> | OR  |
| <input type="radio"/>            | NOT |
| <input type="radio"/>            | NOR |

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Question # 14 of 30 ( Start time: 02:47:12 PM, 28 December 2020 )

Total Marks: 1

Which of the following is the MSI version of the Look-Ahead Carry Generator, which provides identical inputs and outputs except for the C4 output?

Select the correct option

- |                                  |         |
|----------------------------------|---------|
| <input type="radio"/>            | 74XX180 |
| <input type="radio"/>            | 74XX181 |
| <input checked="" type="radio"/> | 74XX182 |
| <input type="radio"/>            | 74XX183 |

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Question # 30 of 30 ( Start time: 03:00:20 PM, 28 December 2020 )

Total Marks: 1

When the error condition is not detected in BCD adder, the circuit output is 0 and the 2nd Adder input B is set to \_\_\_\_\_.

Select the correct option

|                                  |      |  |
|----------------------------------|------|--|
| <input checked="" type="radio"/> | 0000 |  |
| <input type="radio"/>            | 0001 |  |
| <input type="radio"/>            | 0010 |  |
| <input type="radio"/>            | 0011 |  |

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Question # 16 of 30 ( Start time: 02:49:06 PM, 28 December 2020 )

Total Marks: 1

The 4-variable Karnaugh Map (K-Map) has \_\_\_\_\_ cells for min or max terms.

Select the correct option

|                                  |    |
|----------------------------------|----|
| <input type="radio"/>            | 4  |
| <input type="radio"/>            | 8  |
| <input type="radio"/>            | 12 |
| <input checked="" type="radio"/> | 16 |

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## Question # 19 of 30 ( Start time: 02:51:33 PM, 28 December 2020 )

Total Marks: 1

In the Repeated-Division method, the Decimal number to be converted is divided by the \_\_\_\_\_.

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Select the correct option

- |                                  |                                             |  |
|----------------------------------|---------------------------------------------|--|
| <input checked="" type="radio"/> | Base number                                 |  |
| <input type="radio"/>            | 2                                           |  |
| <input type="radio"/>            | Maximum range value of target number system |  |
| <input type="radio"/>            | Minimum range value of target number system |  |

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Question # 15 of 30 ( Start time: 10:33:57 PM, 28 December 2020 )

Total Marks: 1

Adding two octal numbers "36" and "71" result in \_\_\_\_\_

Select the correct option

- |                                  |     |
|----------------------------------|-----|
| <input type="radio"/>            | 213 |
| <input type="radio"/>            | 123 |
| <input checked="" type="radio"/> | 127 |
| <input type="radio"/>            | 345 |

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Question # 4 of 30 ( Start time: 10:25:32 PM, 28 December 2020 )

Total Marks: 1

The expression  $F=A+B+C$  describes the operation of three bits \_\_\_\_ Gate.

Select the correct option

|                                  |      |
|----------------------------------|------|
| <input checked="" type="radio"/> | OR   |
| <input type="radio"/>            | AND  |
| <input type="radio"/>            | NOT  |
| <input type="radio"/>            | NAND |

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Question # 23 of 30 ( Start time: 02:55:22 PM, 28 December 2020 )

Total Marks: 1

In 16-bit ALU, the Carry Out 16 is generated only when ALU3 has received Carry In \_\_\_\_.

Select the correct option

|                                  |    |
|----------------------------------|----|
| <input type="radio"/>            | 2  |
| <input type="radio"/>            | 4  |
| <input type="radio"/>            | 8  |
| <input checked="" type="radio"/> | 12 |

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Question # 29 of 30 ( Start time: 02:59:46 PM, 28 December 2020 )

Total Marks: 1

A SOP expression having a domain of 3 variables will have a truth table having \_\_\_\_ combinations of inputs and corresponding output values.

Select the correct option

|                                  |    |  |
|----------------------------------|----|--|
| <input checked="" type="radio"/> | 2  |  |
| <input type="radio"/>            | 4  |  |
| <input type="radio"/>            | 8  |  |
| <input type="radio"/>            | 16 |  |

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Question # 10 of 30 ( Start time: 02:44:20 PM, 28 December 2020 )

Total Marks: 1

The Quine-McCluskey method has \_\_\_\_\_ number of steps.

Select the correct option

|                                  |   |
|----------------------------------|---|
| <input checked="" type="radio"/> | 2 |
| <input type="radio"/>            | 3 |
| <input type="radio"/>            | 4 |
| <input type="radio"/>            | 5 |

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## Question # 5 of 30 ( Start time: 02:39:11 PM, 28 December 2020 )

Total Marks: 1

To determine the seven expressions for each of the seven outputs in 7-segment display, seven \_\_\_\_-variable Karnaugh maps are used.

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Select the correct option

|                                  |   |  |
|----------------------------------|---|--|
| <input type="radio"/>            | 3 |  |
| <input checked="" type="radio"/> | 4 |  |
| <input type="radio"/>            | 5 |  |
| <input type="radio"/>            | 2 |  |

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## Question # 13 of 30 ( Start time: 10:31:42 PM, 28 December 2020 )

Total Marks: 1

If the number of samples that are collected is reduced by half, the reconstructed signal will be \_\_\_\_\_ from/to the original.

Select the correct option

|                                  |           |
|----------------------------------|-----------|
| <input checked="" type="radio"/> | Different |
| <input type="radio"/>            | Same      |
| <input type="radio"/>            | Equal     |
| <input type="radio"/>            | Opposite  |

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Question # 25 of 30 ( Start time: 02:56:27 PM, 28 December 2020 )

Total Marks: 1

The Binary number 1011.101 has an Integer part represented by \_\_\_\_\_ and a fraction part \_\_\_\_\_ separated by a decimal point.

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Select the correct option

|                                  |           |  |
|----------------------------------|-----------|--|
| <input checked="" type="radio"/> | 1011, 101 |  |
| <input type="radio"/>            | 101, 1011 |  |
| <input type="radio"/>            | 101, 1101 |  |
| <input type="radio"/>            | 10111, 11 |  |

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Question # 20 of 30 ( Start time: 10:38:49 PM, 28 December 2020 )

Total Marks: 1

\_\_\_\_\_ Device dissipate varying amount of power depending upon the frequency of operation.

Select the correct option

|                                  |            |
|----------------------------------|------------|
| <input type="radio"/>            | TTL        |
| <input checked="" type="radio"/> | CMOS       |
| <input type="radio"/>            | Storage    |
| <input type="radio"/>            | Peripheral |

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