

VIRTUAL UNIVERSITY OF PAKISTAN

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CORRECT ANSWER SOLVED BY HADI
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TEAM HADI(VU LAHORE)

MID TERM PAST PAPER

We Deal All Kind of VU Project
(PHP, .NET, ANDROID, PYTHON)

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WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question No : 1 of 26

Marks: 1 (Budgeted Time 1 Min)

In instruction ADC the operands can be

Answer (Please select your correct option)

- ☐ Two registers only
- ☐ Two register and one memory location
- ☐ CF and two other operands
- ☐ ZF and two other operands

correct

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Question No : 2 of 26

Marks: 1 (Budgeted Time 1 Min)

The attribute byte has

Answer (Please select your correct option)

☐ 32 possible background colors

☐ 16 possible background colors

☐ 8 possible background colors

☐ 4 possible background colors

correct

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Question No : 3 of 26

Marks: 1 (Budgeted Time 1 Min)

Memory cell width refers to _____

Answer (Please select your correct option)

☐ Number of bits in a cell

correct

☐ The maximum value that can be stored in a cell including sign-bit

☐ The maximum value that can be stored in a cell excluding sign-bit

☐ Address of that cell

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Question No : 4 of 26

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true about Interrupt Flag

Answer (Please select your correct option)

☐ It tells whether processor can be interrupted from outside or not

correct

☐ It tells whether processor has interrupted or not

☐ It is cleared when an interrupt occurs from outside

☐ It is set when processor wants to generate an interrupt

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Question No : 5 of 26

Marks: 1 (Budgeted Time 1 Min)

When the operand of DIV instruction is of 16 bits then implied dividend will be of

Answer (Please select your correct option)

☐ 8 bits

correct

☐ 16 bits

☐ 32 bits

☐ 64 bits

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Question No : 6 of 26

Marks: 1 (Budgeted Time 1 Min)

If $AX=5$, $BX=5$, CF and ZF are set, AF and DX contains zero then after the execution of instruction "ADC AX, BX", AX will contain the value:

Answer (Please select your correct option)

5

☐

10

☐

11

☐

12

☐

correct

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Question No : 7 of 26

Marks: 1 (Budgeted Time 1 Min)

MS DOS uses ____ display mode.

Answer (Please select your correct option)

☐ Character based

correct

☐ Graphics based

☐ Numeric based

☐ Console based

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Question No : 8 of 26

Marks: 1 (Budgeted Time 1 Min)

Each screen location corresponds to a word, the lower byte of this word contains ____

Answer (Please select your correct option)

☒ The character code

correct

☐ The attribute byte

☐ The parameters

☐ The dimensions

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Question No : 9 of 26

Marks: 1 (Budgeted Time 1 Min)

Which of the following options contain the set of instructions to open a window to the video memory?

Answer (Please select your correct option)

☐ `mov AX, 0xb008`
`mov ES, AX`

☐ `mov AX, 0xb800`
`mov ES, AX`

correct

☐ `mov AX, 0x8b00`
`mov ES, AX`

☐ `mov AX, 0x800b`
`mov ES, AX`

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Question No : 10 of 26

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, SI is decremented or incremented by

Answer (Please select your correct option)

☐ 1

☐ 2

correct

☐ 3

☐ 4

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Question No : 11 of 26

Marks: 1 (Budgeted Time 1 Min)

8088 processor divides interrupts into ____ classes.

Answer (Please select your correct option)

2

correct

3

4

5

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Question No : 12 of 26

Marks: 1 (Budgeted Time 1 Min)

MOVS instruction transfers a byte or word from which of the following source locations.

Answer (Please select your correct option)

☐ ES:DI

☐ DS:DI

☐ ES:SI

☐ DS:SI

correct

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Question No : 13 of 26

Marks: 1 (Budgeted Time 1 Min)

Which of the following is the pair of register used to access memory in string instructions?

Answer (Please select your correct option)

☐ DI and BP

☐ DI and SI

☐ SI and BP

☐ DS and SI

correct

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Question No : 14 of 26

Marks: 1 (Budgeted Time 1 Min)

When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that

Answer (Please select your correct option)

- ☐ BX is greater
- ☒ Both AX and BX are equal
- ☐ ZF has no relation with CMP
- ☐ AX is greater

correct

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Question No : 15 of 26

Marks: 1 (Budgeted Time 1 Min)

Which Register(s) is/are changed during execution of the CALL instruction?

Answer (Please select your correct option)

☐ IP only

☐ IP and SP only

☐ SP only

☐ BP and IP only

correct

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Question No : 16 of 26

Marks: 1 (Budgeted Time 1 Min)

Which of the following is an illegal instruction ?

Answer (Please select your correct option)

☐ PUSH AX

☐ PUSH BX

☐ PUSH DL

☐ PUSH DX

correct

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Question No : 17 of 26

Marks: 1 (Budgeted Time 1 Min)

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

☐ Single word can not be pushed on the stack

correct

☐ Single word can not be popped from the stack

☐ Single byte can not be popped from the stack

☐ Single quad can be popped from the stack

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Question No : 18 of 26

Marks: 1 (Budgeted Time 1 Min)

ASCII Stands for _____

Answer (Please select your correct option)

☐ American Standard Code for Information Interchange

correct

☐ American Scientific Code for International Interchange

☐ American Standard Code for Intelligence Interchange

☐ American Scientific Code for Information Interchange

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Question No : 19 of 26

Marks: 1 (Budgeted Time 1 Min)

In assembly the CX register is used normally as a _____ register.

Answer (Please select your correct option)

☐ source

☐ counter

☐ index

☐ pointer

correct

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Question No : 20 of 26

Marks: 1 (Budgeted Time 1 Min)

How many characters were defined by standard ASCII?

Answer (Please select your correct option)

☐ 64

☒ 128

☐ 256

☐ 512

correct

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Question No : 21 of 26

Marks: 2 (Budgeted Time 4 Min)

How 16-bit DIV operation is different from 32-bit DIV operation?

Answer ([Please click here to Add Answer](#))



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Question No : 22 of 26

Marks: 2 (Budgeted Time 4 Min)

Which of the following are bitwise operators?

AND
MOV
ADD
MUL

Answer ([Please click here to Add Answer](#))



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Question No : 23 of 26

Marks: 3 (Budgeted Time 6 Min)

How the value of Instruction Pointer register is saved and updated when a CALL instruction is executed?

Answer ([Please click here to Add Answer](#))



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Question No : 24 of 26

Marks: 3 (Budgeted Time 6 Min)

For what purpose "INT 1" is reserved ?

Answer (Please [click here](#) to Add Answer)



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Suppose AL contains 11001011b and CF= 1. Give the new contents of AL after each of the following instructions is executed. Assume the preceding initial conditions for each part of this question.

- SHL AL,1
- a. SHR AL,1
- b. ROL AL,CL ;if CL contains 2
- c. SAR AL,CL ;if CL contains 2
- d. RCR AL,CL ;if CL contains 3

Answer ([Please click here to Add Answer](#))



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Following instructions are taken from the program of printing a "hello word" at desired screen location. What functionality is achieved by the following code lines?

[Initially SI is pointing to the string and DI is pointing to the top left column of the screen]

```
mov al, [si]
mov [es:di], ax
add di, 2
```

Answer ([Please click here to Add Answer](#))



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Question No : 26 of 26

Marks: 5 (Budgeted Time 10 Min)

[Initially SI is pointing to the string and DI is pointing to the top left column of the screen]

```
mov al, [si]
mov [es:di], ax
add di, 2
add si, 1
```

Answer ([Please click here to Add Answer](#))



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Question No : 22 of 26

Marks: 2 (Budgeted Time 4 Min)

OR
NOT
SUB
XOR
DIV
ADC

Answer ([Please click here to Add Answer](#))



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