

Learning4help by Waqas

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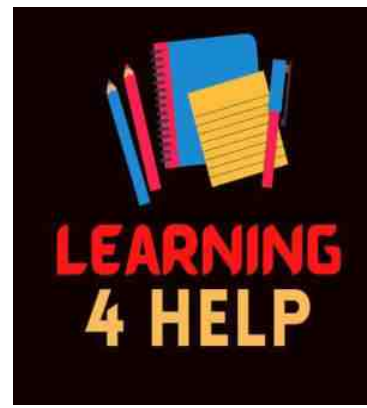
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Question # 15 of 15 (Start time: 09:32:55 PM, 31 January 2023)

Total Marks: 1

The terminal count of a 4-bit binary counter in the DOWN mode is _____

Select the correct option

☐ 0011

☐ 1100

☒ 0000

☐ 1111

Question # 14 of 15 (Start time: 09:31:37 PM, 31 January 2023)

Total Marks: 1

The terminal count of a 4-bit binary counter in the UP mode is _____

Select the correct option

☐ 0000

☐ 0011

☒ 1111

☐ 1100

The combinational digital circuits have _____ storage element; therefore combinational circuits handle only instantaneous inputs.

Select the correct option

- ☐ one
- ☐ two
- ☐ three
- ☒ no

Click to Save Answer & Move to Next Question

The duration for which the elevator doors are opened, and remain open, and time it takes for the elevator to move from one floor to the next can be determined by a/an _____.

Select the correct option

- ☐ Input Signal
- ☐ Output Signal
- ☐ None of the given options
- ☒ Clock Signal

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Select the correct option

- ☐ Input Signal
- ☐ Output Signal
- ☐ None of the given options
- ☒ Clock Signal

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Question # 11 of 15 (Start time: 09:28:42 PM, 31 January 2023) Total Marks: 1

To serially shift a byte of data into a shift register, there must be

Select the correct option

- ☒ one clock pulse
- ☐ eight clock pulses
- ☐ one clock pulse for each 1 in the data
- ☐ one load pulse

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Question # 10 of 15 (Start time: 09:27:23 PM, 31 January 2023) Total Marks: 1

In gated SR latch, what is the value of the output if EN=1, S=0 and R=1?

Select the correct option

- ☐ 1
- ☐ Invalid
- ☒ 0
- ☐ Qt

To parallel load a byte of data into a shift register, there must be

Select the correct option

- ☐ one clock pulse for each 0 in the data
- ☐ one clock pulse for each 1 in the data
- ☒ one clock pulse
- ☐ eight clock pulse

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For a down counter that counts from (111 to 000), if current state is "101" the next state will be _____

Select the correct option

- ☐ 010
- ☐ none of given options
- ☐ 111
- ☒ 110

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Question # 3 of 15 (Start time: 09:20:25 PM, 31 January 2023) Total Marks: 1

RCO stands for _____

Select the correct option

- ☒ Ripple Clock Output
- ☐ Reconfiguration Counter Output
- ☐ Reconfiguration Clock Output
- ☐ Ripple Counter Output

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Question # 2 of 15 (Start time: 09:16:42 PM, 31 January 2023)

Total Marks: 1

A stable multi-vibrator is an Oscillator which does not have any _____

Select the correct option

- ☒ Stable state
- ☐ Flip flop
- ☐ Unstable state
- ☐ Output

Question # 1 of 15 (Start time: 09:16:23 PM, 31 January 2023)

Total Marks: 1

_____ occurs when the same clock signal arrives at different times at different clock inputs due to propagation delay.

Select the correct option

- ☒ Clock Skew
- ☐ Race condition
- ☐ None of given options
- ☐ Ripple Effect

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Question # 10 of 15 (Start time: 10:37:44 PM, 31 January 2023) Total Marks: 1

The OLMC of the GAL16V8 is similar to the OLMC of the _____ with some enhancements.

Select the correct option

- ☒ GAL22V10
- ☐ GAL12V10
- ☐ GAL16V10
- ☐ GAL14V10

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Question # 9 of 15 (Start time: 10:37:20 PM, 31 January 2023) Total Marks: 1

Divide-by-32 counter can be achieved by using

Select the correct option

- ☐ Flip-Flop and DIV 16
- ☒ Flip-Flop and DIV 10
- ☐ Flip-Flop and DIV 32
- ☐ DIV 16 and DIV 32

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A transparent mode means.....

Select the correct option

- ☒ The changes in the data at the inputs of the latch are seen at the output
- ☐ Input Hold time is zero (no need to maintain input after clock transition)
- ☐ The changes in the data at the inputs of the latch are not seen at the output
- ☐ Propagation Delay is zero (Output is immediately changed when clock signal is applied)

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Karnaugh map is used in designing

Select the correct option

- ☐ an UP/DOWN counter
- ☐ a counter
- ☒ All of the above
- ☐ a clock

Click to Save Answer & Move to Next Question

Question # 2 of 15 (Start time: 10:32:39 PM, 31 January 2023)

Total Marks: 1

In gated SR latch, what is the value of the output if $EN=1$, $S=0$ and $R=1$?

Select the correct option

- ☒ 1
- ☐ 0
- ☐ Invalid
- ☐ Qt

when the transmission line is idle in an asynchronous transmission

Select the correct option

- ☐ It is set to logic low
- ☒ It is set to logic high
- ☐ State of transmission line is not used to start transmission
- ☐ It remains in previous state

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Question # 15 of 15 (Start time: 10:40:12 PM, 31 January 2023) Total Marks: 1

The minimum time required for the input logic levels to remain stable before the clock transition occurs is known as the _____

Select the correct option

☒	Set-up time
☐	Hold time
☐	Pulse Stability time (PST)
☐	Pulse Interval time

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13... ENG 10:40 PM

The GAL16V8 has_____.

Select the correct option

- ☐ 16 dedicated inputs
- ☐ 8 pins that are used as inputs or outputs
- ☐ 8 special function pins
- ☒ All of the given options

Question # 9 of 10 (Start time: 12:22:36 AM, 07 July 2023) Total Marks: 1

The 74HC163 is a 4-bit Synchronous Counter.it has_____parallel data inputs pins

Select the correct option

- ☐ 6
- ☐ 8
- ☒ 4
- ☐ 2

The operation of J-K flip-flop is similar to that of the SR flip-flop except that the J-K flip-flop _____

Select the correct option

- ☐ It does not show transition on change in pulse
- ☒ Doesn't have an invalid state
- ☐ Sets to clear when both $J = 0$ and $K = 0$
- ☐ It does not accept asynchronous inputs

Question # 7 of 10 (Start time: 12:20:42 AM, 07 July 2023) Total Marks: 1

In 16-input Multiplexer, the four outputs are connected together through a 4-input _____ gate.

Select the correct option

- ☒ OR
- ☐ AND
- ☐ NAND
- ☐ NOT

Question # 5 of 10 (Start time: 12:17:41 AM, 07 July 2023) Total Marks: 1

The PROM consists of a fixed non-programmable _____ Gate array configured as a decoder.

Select the correct option

- ☐ NOT
- ☒ AND
- ☐ XOR
- ☐ OR

A Quad 1-of-4 MUX has four Multiplexers, each multiplexer has _____ inputs and a single output.

Select the correct option

- ☐ Six
- ☐ Five
- ☒ Four
- ☐ Eight

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A Quad 1-of-4 MUX has four Multiplexers, each multiplexer has _____ inputs and a single output.

Select the correct option

- ☐ Six
- ☐ Five
- ☒ Four
- ☐ Eight

_____ come in different configurations they are identified by unique number

Select the correct option

- ☐ GAL
- ☒ PALs
- ☐ PLAs
- ☐ EPROM

Design of state diagram is one of many steps used to design

Select the correct option

- ☒ a truncated counter
- ☐ a clock
- ☐ any counter
- ☐ an UP/DOWN counter

In Master-Slave flip-flop setup, the master flip flop operates at _____

Select the correct option

- ☐ Negative half cycle of pulse
- ☐ Both Master-Slave operate simultaneously
- ☐ Master-Slave flip-flop does not operate on pulses, rather it is edge triggered.
- ☒ Positive half cycle of pulse

Question # 10 of 10 (Start time: 12:07:55 AM, 07 July 2023) Total Marks: 1

In case of cascading Integrated Circuit counters, the enable inputs and RCO of the Integrated Circuit counters allow cascading of multiple counters together

Select the correct option

☒ True

☐ False

The latch is said to be in logic _____ state when $Q=1$ and $Q'=0$ and it is in the logic low state when $Q=0$ and $Q'=1$.

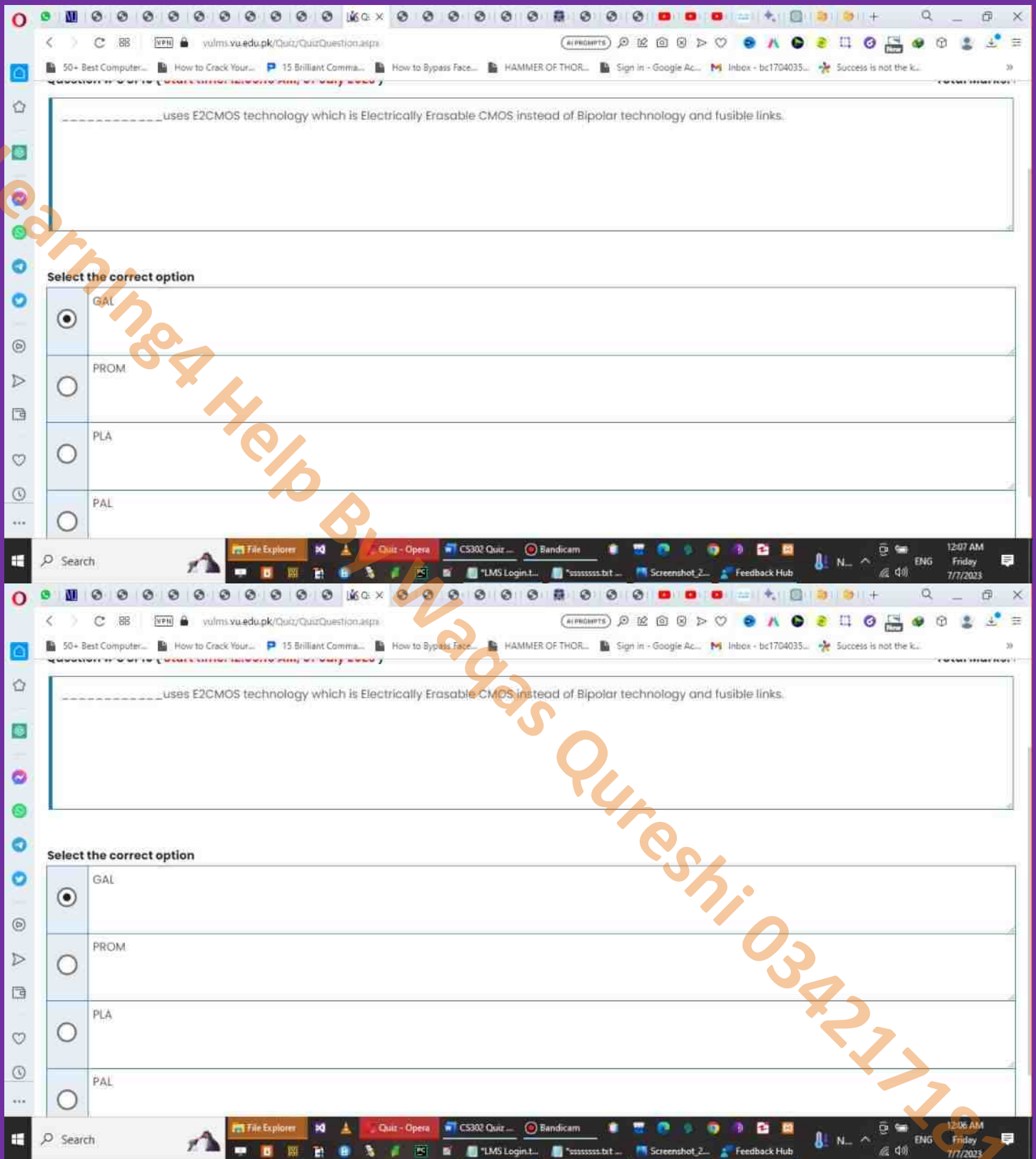
Select the correct option

☐ low

☒ high

☐ constant

☐ zero



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In 1-to-4 demultiplexer, how many select lines are required

Select the correct option

- ☐ 4
- ☐ 3
- ☐ 5
- ☒ 2

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In NAND based S-R latch, output of each _____ gate is connected to the input of the other _____ gate.

Select the correct option

- ☐ NAND,NOR
- ☐ NOR,NAND
- ☒ NAND,NAND
- ☐ NOR,NOR

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The GAL22V10 device is available as low-voltage _____ version.

Select the correct option

☐ 1.5v

☒ 3.3v

☐ 2.0v

☐ 3.0v

Question # 5 of 10 (Start time: 12:04:14 AM, 07 July 2023)

Total Marks: 1

The _____ description is used to simulate the logic circuit and verify its operation.

Select the correct option

☐ Test file

☐ Declaration

☒ Test vector

☐ Logic

Question # 4 of 10 (Start time: 12:03:28 AM, 07 July 2023) Total Marks: 1

The GAL22V10 is a popular GAL device having _____ inputs and ten inputs/outputs.

Select the correct option

☒ 12

☐ 22

☐ 20

☐ 10

Question # 5 of 10 (Start time: 12:03:28 AM, 07 July 2023) Total Marks: 1

_____ is used when the output is connected back to the input of the PAL or if the output pin is used as an input only.

Select the correct option

☐ Combinational Input

☐ Combinational Output

☐ Programmable polarity

☒ Combinational Input/Output

Question # 2 of 10 (Start time: 12:00:59 AM, 07 July 2023) Total Marks: 1

The n flip-flops store _____ states.

Select the correct option

☐ $2^{n(n+1)}$

☐ 1

☐ 0

☒ 2^n

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