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Midterm Grand Quiz: <https://arapna.com/papers/mid-term-paper/>

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 88 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 1 of 30 (Start time: 05:59:20 PM, 27 December 2020)

Total Marks: 1

SPEC stands for _____

Select the correct option

- ☐ Scientific Performance Execution Cooperative
- ☐ System Performance Execution Cooperative
- ☐ Scientific Performance Evaluation Cooperative
- ☐ System Performance Evaluation Cooperative

Click to Save Answer & Move to Next Question

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CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 2 of 30 (Start time: 06:00:19 PM, 27 December 2020)

Total Marks: 1

Which of the following register(s) is/are programmer invisible and is/are required to hold an operand or result value while the bus is busy transmitting some other value?

Select the correct option

- ☐ Instruction Register
- ☐ Memory address register
- ☐ Memory Buffer Register
- ☐ Registers A and C

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 3 of 30 (Start time: 06:01:17 PM, 27 December 2020) Total Marks: 1

Which one of the following circuit design levels is called the gate level?

Select the correct option

☐ Logic Design Level

☐ Circuit Level

☐ Mask Level

☐ Machine Level

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 4 of 30 (Start time: 06:02:15 PM, 27 December 2020) Total Marks: 1

Program counter (PC) holds the memory address of _____?

Select the correct option

☐ Previous instruction

☐ Current instruction

☐ Next instruction

☐ Previous and current instruction

Click to Save Answer & Move to Next Question

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The screenshot shows a web browser window with multiple tabs. The active tab is titled 'Quiz' and displays a quiz interface for 'MC190401335: IQRA SHAHZADI'. The quiz is titled 'CS501: Grand Quiz' and has a 'Time Left' of 89 seconds. The start time is '05:59 PM, 27 December 2020'. The current question is 'Question # 5 of 30 (Start time: 06:03:02 PM, 27 December 2020)' with 'Total Marks: 1'. The question text is: '_____ provides a temporary storage for the address of memory location to be accessed.' Below the question, there are four radio button options: MAR, MBR, PC, and LPC. A button at the bottom right says 'Click to Save Answer & Move to Next Question'. The browser's address bar shows the URL 'quiz.vu.edu.pk/QuizQuestion.aspx?ver=072c86d2-369a-416d-89b0-afd36b081a77'. The Windows taskbar at the bottom shows the time as 6:03 PM on 12/27/2020.

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 5 of 30 (Start time: 06:03:02 PM, 27 December 2020) Total Marks: 1

_____ provides a temporary storage for the address of memory location to be accessed.

Select the correct option

☐ MAR

☐ MBR

☐ PC

☐ LPC

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 88 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 6 of 30 (Start time: 06:04:24 PM, 27 December 2020) Total Marks: 1

The _____ control signal enables the input to the register C for writing the incremented value of PC onto it.

Select the correct option

☐ INC4

☐ LC

☐ PCout

☐ LPC

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 7 of 30 (Start time: 06:05:59 PM, 27 December 2020) Total Marks: 1

In SRC, the general-purpose register file includes _____ registers, each 32 bit wide.

Select the correct option

- ☐ 16 registers R0 to R15
- ☐ 24 registers R0 to R23
- ☐ 32 registers R0 to R31
- ☐ 64 registers R0 to R63

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 8 of 30 (Start time: 06:06:44 PM, 27 December 2020) Total Marks: 1

Which of the following notations represents the indexed or scaled addressing mode in any processor?

Select the correct option

- ☐ $[[R4], \{R5\}]$
- ☐ $M[R1+25]$
- ☐ $M[R2]$
- ☐ $\{123\}$

Click to Save Answer & Move to Next Question

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CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 9 of 30 (Start time: 06:08:16 PM, 27 December 2020) Total Marks: 1

Which of the following is responsible for generating signals for external events?

Select the correct option

- ☐ Exception generator
- ☒ Interrupt generator
- ☐ "CON" control signal
- ☐ Control unit signals generator

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 70 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 10 of 30 (Start time: 06:10:09 PM, 27 December 2020) Total Marks: 1

Super-scalar processor divides the instructions into _____ classes.

Select the correct option

- ☐ 2
- ☒ 3
- ☐ 4
- ☐ 5

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 12 of 30 (Start time: 06:11:35 PM, 27 December 2020) Total Marks: 1

Total number of data registers in Motorola 68000 processor are _____

Select the correct option

☐ 8

☐ 12

☐ 24

☐ 32

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 13 of 30 (Start time: 06:12:12 PM, 27 December 2020) Total Marks: 1

To set the value of micro-PC from branch address, the value of 4 to 1 multiplexer is _____

Select the correct option

☐ 00

☐ 01

☐ 10

☐ 11

Click to Save Answer & Move to Next Question

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The image displays two screenshots of a web-based quiz interface. The top screenshot shows Question # 14 of 30, asking: "Which of the following directives is used in FALSIM to define a variable?". The options are EQU, DW, DD, and ORG. The bottom screenshot shows Question # 15 of 30, asking: "In Falcon-A processor, first 5 most significant bits from the IR are fed to a _____ decoder.". The options are 5-to-32, 5-to-10, 5-to-31, and 11-to-15. Both screenshots show a "Click to Save Answer & Move to Next Question" button.

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Quiz Start Time: 05:59 PM, 27 December 2020

Time Left: 80 sec(s)

Question # 14 of 30 (Start time: 06:13:36 PM, 27 December 2020) Total Marks: 1

Which of the following directives is used in FALSIM to define a variable?

Select the correct option

- ☐ EQU
- ☐ DW
- ☐ DD
- ☐ ORG

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Quiz Start Time: 05:59 PM, 27 December 2020

Time Left: 87 sec(s)

Question # 15 of 30 (Start time: 06:14:38 PM, 27 December 2020) Total Marks: 1

In Falcon-A processor, first 5 most significant bits from the IR are fed to a _____ decoder.

Select the correct option

- ☐ 5-to-32
- ☐ 5-to-10
- ☐ 5-to-31
- ☐ 11-to-15

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 88 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 16 of 30 (Start time: 06:16:01 PM, 27 December 2020) Total Marks: 1

In SRC, the memory is accessed in the chunks of _____ byte(s) each.

Select the correct option

☐ 2

☐ 4

☐ 16

☐ 32

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 17 of 30 (Start time: 06:16:51 PM, 27 December 2020) Total Marks: 1

Which of the following control signals is NOT activated during instruction fetch operation?

Select the correct option

☐ PCout

☐ LC

☐ LMAR

☐ Cout

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Quiz Start Time: 05:59 PM, 27 December 2020

Time Left 89 sec(s)

Question # 18 of 30 (Start time: 06:17:36 PM, 27 December 2020) Total Marks: 1

In Type C instruction of SRC, _____ bits are allocated for constant value.

Select the correct option

- ☐ 16
- ☐ 17
- ☐ 21
- ☐ 22

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Quiz Start Time: 05:59 PM, 27 December 2020

Time Left 89 sec(s)

Question # 19 of 30 (Start time: 06:18:35 PM, 27 December 2020) Total Marks: 1

Which type of exceptions rise during the process of decoding and executing the instruction?

Select the correct option

- ☐ Interrupts (External Exceptions)
- ☐ Hardware Exceptions
- ☐ Non-mask able Exceptions
- ☐ Program Exceptions

Click to Save Answer & Move to Next Question

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The image displays two screenshots of a web-based quiz interface. The top screenshot shows Question #20 of 30, asking about the register used in the 'DIV R3' instruction of the EAGLE processor. The bottom screenshot shows Question #22 of 30, asking for the instruction length of the FALCON-E processor. Both screenshots show a timer at 89 seconds and a 'Click to Save Answer & Move to Next Question' button.

Question #20 of 30 (Start time: 06:19:28 PM, 27 December 2020) Total Marks: 1

In "DIV R3" instruction of EAGLE, the register _____ is used as both source operand and destination operand.

Select the correct option

- ☐ R0
- ☐ R1
- ☐ R2
- ☐ R3

Click to Save Answer & Move to Next Question

Question #22 of 30 (Start time: 06:20:39 PM, 27 December 2020) Total Marks: 1

What is the instruction length of the FALCON-E processor?

Select the correct option

- ☐ 8 bits
- ☐ 16 bits
- ☐ 32 bits
- ☐ 64 bits

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 23 of 30 (Start time: 06:21:44 PM, 27 December 2020) Total Marks: 1

Which type of instructions enables mathematical computations?

Select the correct option

- ☐ Arithmetic
- ☐ Control
- ☐ Data transfer
- ☐ Numeric

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 24 of 30 (Start time: 06:22:23 PM, 27 December 2020) Total Marks: 1

Which are the two control unit design approaches?

Select the correct option

- ☐ Cache and RAM
- ☐ Registers and instructions
- ☐ Hardwired and micro programmed
- ☐ Control signals and busses

Click to Save Answer & Move to Next Question

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The screenshot shows a web browser window with multiple tabs. The active tab is a quiz page from 'quiz.vu.edu.pk'. The page header includes the user ID 'MC190401335: IQRA SHAHZADI' and the quiz title 'CS501: Grand Quiz'. A timer indicates 'Time Left: 88 sec(s)' and the start time is '05:59 PM, 27 December 2020'. The question is 'Question # 27 of 30 (Start time: 06:26:13 PM, 27 December 2020)' with 'Total Marks: 1'. The question text is: '_____ control signal allows the contents of the Program Counter register to be written onto the internal processor bus.' Below the question, there are four radio button options: 'INC4', 'LPC', 'PCout', and 'LC'. At the bottom of the question area, there is a button that says 'Click to Save Answer & Move to Next Question'. The browser's taskbar at the bottom shows the Windows logo, several application icons, and the system clock indicating '6:26 PM 12/27/2020'.

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left: 88 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 27 of 30 (Start time: 06:26:13 PM, 27 December 2020) Total Marks: 1

_____ control signal allows the contents of the Program Counter register to be written onto the internal processor bus.

Select the correct option

- ☐ INC4
- ☐ LPC
- ☐ PCout
- ☐ LC

Click to Save Answer & Move to Next Question

MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left: 88 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 28 of 30 (Start time: 06:27:21 PM, 27 December 2020) Total Marks: 1

Execution time of a program with respect to the processor is calculated as:

Select the correct option

- ☐ Execution Time = IC x CPI x MIPS
- ☐ Execution Time = IC x CPI x T
- ☐ Execution Time = CPI x T x MFLOPS
- ☐ Execution Time = IC x T

Click to Save Answer & Move to Next Question

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MC190401335: IQRA SHAHZADI

CS501: Grand Quiz

Time Left 89 sec(s)

Quiz Start Time: 05:59 PM, 27 December 2020

Question # 30 of 30 (Start time: 06:29:20 PM, 27 December 2020) Total Marks: 1

Among the two approaches available to design a control unit, hardwired approach is relatively _____ as compared to micro-programming.

Select the correct option

☐ Slow

☐ Average

☐ Faster

☐ Better

Click to Save Answer & Move to Next Question

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