

File by MAM MEHWISH 03184148783
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1. Two 2-input, 4-bit multiplexers 74X157 can be connected to implement a ____ multiplexer.

Options:

- a) 4-input, 16-bit
- b) 2-input, 8-bit
- c) 4-input, 8-bit
- d) 2-input, 4-bit

Correct option: a) 4-input, 16-bit

2. _____ is said to occur when multiple internal variables change due to change in one input variable.

Options:

- a) Clock Skew
- b) Race condition
- c) Hold delay
- d) Hold and Wait

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Correct option: b) Race condition

3. The latch is said to be in logic _____ state when $Q=1$ and $Q'=0$, and it is in the logic low state when $Q=0$ and $Q'=1$.

Options:

- a) Low
- b) High
- c) Constant
- d) Zero

Correct option: b) High

4. Asynchronous means that _____.

Options:

- a) Each flip-flop after the first one is enabled by the output of the preceding flip-flop
- b) Each alternative flip-flop after the first one is enabled by the output of the preceding flip-flop
- c) Each flip-flop is enabled by the output of the preceding flip-flop
- d) Each flip-flop except the last one is enabled by the output of the preceding flip-flop

Correct option: c) Each flip-flop is enabled by the output of the preceding flip-flop

5. A _____ can not operate without a memory element.

Options:

- a) Unit
- b) Clock
- c) Cell
- d) Counter

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Correct option: d) Counter

6. _____ flip-flops are obsolete now.

Options:

- a) Edge-triggered
- b) T-Flipflop
- c) Master-Slave
- d) D-Flipflop

Correct option: c) Master-Slave

7. _____ occurs when the same clock signal arrives at different times at different clock inputs due to propagation delay.

Options:

- a) Clock Skew
- b) Ripple Effect
- c) Race condition

Correct option: a) Clock Skew

8. An Asynchronous Down-counter is implemented (Using J-K flip-flop) by connecting _____ Q output of all flip-flops to the clock input of the next flip-flops.

Options:

- a) Q' output of all flip-flops to clock input of next flip-flops
- b) Q output of all flip-flops to J input of next flip-flops
- c) Q' output of all flip-flops to K input of next flip-flops

Correct option: c) Q' output of all flip-flops to K input of next flip-flops

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9. GAL can be reprogrammed as instead of fuses, E2CMOS logic is used which can be programmed to connect a _____ with a _____.

Options:

- a) Column, Row
- b) Row, Column
- c) Row, Row
- d) Column, Column

Correct option: b) Row, Column

10. Three cascaded modulus-10 counters have an overall modulus of _____.

Options:

- a) 100
- b) 1000
- c) 10000
- d) 100000

Correct option: c)

10000

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