

VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future
CORRECT ANSWER SOLVED BY HADI
EMAIL: hadirajputofficial@gmail.com
WHATSAPP& CALL: 03087122922
TEAM HADI(VU LAHORE)

MID TERM PAST PAPER

We Deal All Kind of VU Project
(PHP, .NET, ANDROID, PYTHON)

MADE AND SOLVED BY HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



WhatsApp: 03087122922



FACEBOOK ID: <https://www.facebook.com/hadipastpapers/>

Best of luck!



Question # 2 of 30 (Start time: 09:21:11 PM, 26 December 2020)

Total Marks: 1

UART is a device used for _____ serial communication.

Select the correct option

☐	Synchronous
☒	Asynchronous

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Email:hadirajputofficial@gmail.comWhatsapp:03087122922(Team Hadi Lahore)

Question # 3 of 30 (Start time: 09:22:13 PM, 26 December 2020)

Total Marks: 1

BIOS supports _____ UARTS as COM Ports.

Select the correct option

☐	6
☒	4
☐	3
☐	2

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Question # 9 of 30 (Start time: 09:27:54 PM, 26 December 2020)

Total Marks: 1

The _____ register of Real Time Clock is used to enable interrupt on various events like alarm time and time-up duration.

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Select the correct option

☐	Status Register C	
☐	Status Register A	
☐	Status Register D	
☒	Status Register B	

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Question # 10 of 30 (Start time: 09:28:36 PM, 26 December 2020)

Total Marks:

An I/O device cannot be directly connected to the buses so they generally require a _____ to be placed between the CPU and I/O device.

Select the correct option

☒	Controller
☐	Pipeline
☐	Data Bus
☐	I/O Bus

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Question # 11 of 30 (Start time: 09:29:17 PM, 26 December 2020)

Total Marks: 1

If bit rate in asynchronous serial communication is 9600, then bits are sampled after every _____ seconds.

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Select the correct option

☒	9600	
☐	1/9600	
☐	1/96	
☐	96	

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Question # 12 of 30 (Start time: 09:30:28 PM, 26 December 2020)

Total Marks: 1

The PPI acts as an interface between the CPU and a parallel _____.

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Select the correct option

☒	I/O device	
☐	Controller	
☐	Control bus	
☐	Data bus	

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Question # 13 of 30 (Start time: 09:30:58 PM, 26 December 2020)

Total Marks: 1

If the processor is 386 then the _____ bit of the EFLAGS register will not be set.

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Select the correct option

☐	12th
☒	18th
☐	20th
☐	24th

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Question # 14 of 30 (Start time: 09:31:36 PM, 26 December 2020)

Total Marks:

_____ is used to identify the cause of interrupt.

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Select the correct option

☐ Interrupt Enable Register☒ Interrupt ID Register☐ Interrupt Status Register☐ FLAGS Register**MADE BY HADI**Email: hadirajputofficial@gmail.com Whatsapp: 03087122922 (Team Hadi Lahore)

Question # 16 of 30 (Start time: 09:32:41 PM, 26 December 2020)

Total Marks: 1

_____ interrupt is used to read time from Real Time Clock.

Select the correct option

☒	1A/02H	
☐	1A/03H	
☐	1A/04H	
☐	1A/05H	

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Question # 17 of 30 (Start time: 09:33:13 PM, 26 December 2020)

Total Marks: 1

Hardware Interrupts are _____.

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Select the correct option

☐	Preemptive
☒	Non-Preemptive
☐	Both Preemptive and Non-Preemptive
☐	Neither Preemptive nor Non-Preemptive

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Question # 18 of 30 (Start time: 09:33:44 PM, 26 December 2020)

Total Marks: 1

NMI Stand for _____

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Select the correct option

☐	Non Maximum Interrupt	
☒	Non Maskable Interrupt	
☐	Non Multitude Interrupt	
☐	Non Multiplexed Interrupt	

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Question # 19 of 30 (Start time: 09:34:09 PM, 26 December 2020)

Total Marks: 1

If we want to produce the shrill voice from the speaker phone then we have to load the ____ divisor value at Port ____.

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Select the correct option

☐	high, 0x42	
☐	low, 0x42	
☒	high, 0x22	
☐	low, 0x22	

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Question # 21 of 30 (Start time: 09:35:56 PM, 26 December 2020)

Total Marks: 1

Timer interrupt approximately occurs _____ times every second.

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Select the correct option

☐	10.8	
☐	12.8	
☐	18.0	
☒	18.2	

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Question # 27 of 30 (Start time: 09:42:13 PM, 26 December 2020)

Total Marks:

Interrupt _____ is used to get or set the time.

Select the correct option

☐	0AH
☒	1AH
☐	2AH
☐	3AH

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Question # 28 of 30 (Start time: 09:42:53 PM, 26 December 2020)

Total Marks: 1

DMA is an abbreviation for _____

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Select the correct option

☐	Distributed Memory Access	
☐	Data Managed Access	
☒	Direct Memory Access	
☐	Dual Memory Access	

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Question # 29 of 30 (Start time: 09:43:18 PM, 26 December 2020)

Total Marks:

Interrupt _____ is empty; we can use its vector as a flag.

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Select the correct option



9H



13H



15H



65H

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Question # 30 of 30 (Start time: 09:43:44 PM, 26 December 2020)

Total Marks: 1

By cascading two DMA devices, _____ bits can be transferred.

Select the correct option

☐	4
☐	8
☒	16
☐	32

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Select the correct option

☐	0AH
☒	1AH
☐	2AH
☐	3AH

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Select the correct option

☐	10.8
☐	12.8
☐	18.0
☒	18.2

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Select the correct option



9H



13H



15H



65H

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☐	32

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Select the correct option

☐	Status Register C	
☐	Status Register A	
☐	Status Register D	
☒	Status Register B	

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FOR ASSISTANCE

- 1) ASSIGNMENTS
- 2) QUIZZES
- 3) GDB
- 4) GRAND QUIZZES

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Quizzes, GDBs, Grand Quizzes
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