

Question # 1 of 30 (Start time: 06:36:37 PM, 28 June 2020)

Total Mark

A _____ attached to the Select inputs of the Demultiplexer routes the incoming serial bits to successive outputs where each bit is stored.

Select the correct option



memory



counter



buffer



data

All Answer Are Not Correct (Solve itself)

Question # 2 of 30 (Start time: 06:37:16 PM, 28 June 2020)

Total Mark

Half-Adder Logic circuit contains ____ XOR Gates.

Select the correct option



0



2



1



3

All Answer Are Not Correct (Solve itself)

Question # 3 of 30 (Start time: 06:38:54 PM, 28 June 2020)

Total Mark

How many outputs can an integrated circuit comparator have?

Select the correct option



One



Two



Three



Four

All Answer Are Not Correct (Solve itself)

Question # 4 of 30 (Start time: 06:40:29 PM, 28 June 2020)

Total Mark

TTL based devices work with a dc supply of ____ Volts

Select the correct option



+10



+5



+3



3.3

All Answer Are Not Correct (Solve itself)

Question # 5 of 30 (Start time: 06:41:17 PM, 28 June 2020)

Total Mark

Consider the sum of weight method for converting decimal into binary value, _____ is the highest weight for 411.

Select the correct option



64



128



256



512

All Answer Are Not Correct (Solve itself)

Question # 6 of 30 (Start time: 06:43:14 PM, 28 June 2020)

Total Mark

The simplified expression is presented in a _____ format.

Select the correct option



Truth Table



Function Table



Logic Table



None of above

All Answer Are Not Correct (Solve itself)

Question # 7 of 30 (Start time: 06:44:04 PM, 28 June 2020)

Total Mark

Which of the number is not a representative of hexadecimal system

Select the correct option



"1234"

//



"ABCD"

//



"1001"

//



"DEFH"

//

All Answer Are Not Correct (Solve itself)

Question # 8 of 30 (Start time: 06:45:41 PM, 28 June 2020)

Total Mark

High level Noise Margins (VNH) of CMOS 5 volt series circuits is _____

Select the correct option



0.3 V

//



0.5 V

//



0.9 V

//



3.3 V

//

All Answer Are Not Correct (Solve itself)

Question # 9 of 30 (Start time: 06:46:58 PM, 28 June 2020)

Total Mark

XOR is an abbreviation of _____.

Select the correct option



Inclusive-OR

//



Expendable-NOR

//



Extended-NOR

//



Exclusive-OR

//

All Answer Are Not Correct (Solve itself)

Question # 10 of 30 (Start time: 06:47:28 PM, 28 June 2020)

Total Mark

Excess-8 code assigns _____ to "-8"

Select the correct option



1110



1100



1000



0000

All Answer Are Not Correct (Solve itself)

Question # 11 of 30 (Start time: 06:49:26 PM, 28 June 2020)

Total Mark

A SOP expression having a domain of 3 variables will have a truth table having ____ combinations of inputs and corresponding output values.

Select the correct option



2



4



8



16

All Answer Are Not Correct (Solve itself)

Question # 12 of 30 (Start time: 06:51:00 PM, 28 June 2020)

Total Mark

 $A + B = B + A$ is _____

Select the correct option



Demorgan's Law



Distributive Law



Commutative Law



Associative Law

All Answer Are Not Correct (Solve itself)

Question # 13 of 30 (Start time: 06:51:22 PM, 28 June 2020)

Total Mark

The absence of a _____ element in sequential circuit restricts the use of digital combinational circuits to certain application areas.

Select the correct option



cell



memory



clock



device

All Answer Are Not Correct (Solve itself)

Question # 14 of 30 (Start time: 06:52:17 PM, 28 June 2020)

Total Mark

The three fundamental gates are _____

Select the correct option



AND, NAND, XOR

//



OR, AND, NAND

//



NOT, NOR, XOR

//



NOT, OR, AND

//

All Answer Are Not Correct (Solve itself)

Question # 16 of 30 (Start time: 06:54:31 PM, 28 June 2020)

Total Mark

How many of enable inputs is(are) active-low in 74XX138 3-to-8 Decoder?

Select the correct option



One



Two



Three



Four

All Answer Are Not Correct (Solve itself)

Question # 17 of 30 (Start time: 06:55:42 PM, 28 June 2020)

Total Mark

Any of the _____ forms of the Karnaugh Map can be used to simplify Boolean expressions.

Select the correct option



Three



Four



Two



Five

All Answer Are Not Correct (Solve itself)

Question # 18 of 30 (Start time: 06:57:14 PM, 28 June 2020)

Total Mark

Maxterm is the sum of _____ of the corresponding Minterm with its literal complemented.

Select the correct option



Terms



Words



Numbers



Nibble

All Answer Are Not Correct (Solve itself)

Question # 19 of 30 (Start time: 06:59:05 PM, 28 June 2020)

Total Mark

_____ is an essential part of sequential circuit.

Select the correct option



Memory Element



Clock



Multiplexers



JK Flip Flop

All Answer Are Not Correct (Solve itself)

Question # 20 of 30 (Start time: 07:00:05 PM, 28 June 2020)

Total Mark

The ____ enable input of the decoder when set to 1 disables the decoder and the multiplexers.

Select the correct option



A



B



S



G

All Answer Are Not Correct (Solve itself)

Question # 22 of 30 (Start time: 07:01:39 PM, 28 June 2020)

Total Mark

Inputs		Output
A	B	F
0	0	0
0	1	1
1	0	1
1	1	0

Select the correct option



OR



AND



XOR



NOT

All Answer Are Not Correct (Solve itself)

Question # 23 of 30 (Start time: 07:02:30 PM, 28 June 2020)

Total Mark

Karnaugh Map Array is considered to be wrapped around where all sides are _____ to each other.

Select the correct option



Adjacent



Opposite



Parallel



None of above

All Answer Are Not Correct (Solve itself)

Question # 24 of 30 (Start time: 07:03:18 PM, 28 June 2020)

Total Mark

Cin is part of _____ Adder.

Select the correct option



Half



Full



Single



Double

All Answer Are Not Correct (Solve itself)

Question # 25 of 30 (Start time: 07:05:16 PM, 28 June 2020)

Total Mark

The BCD to 7-Segment Decoder unlike the Binary Decoders activates multiple but unique set of outputs for each _____ BCD input combination.

Select the correct option



1-bit



2-bit



3-bit



4-bit

All Answer Are Not Correct (Solve itself)

Question # 26 of 30 (Start time: 07:06:12 PM, 28 June 2020)

Total Mark

The active high and active low inputs of 3-to-8 Decoder are as follows

Select the correct option



One active-high and the remaining two are active-low



Two active-high and the remaining one is active-low



All active high



All active low

All Answer Are Not Correct (Solve itself)

Question # 27 of 30 (Start time: 07:08:06 PM, 28 June 2020)

Total Mark

A SOP expression can be implemented by an _____ combination of gates.

Select the correct option



OR-XOR

//



AND-NAND

//



AND-OR

//



XOR-NOR

//

All Answer Are Not Correct (Solve itself)

Question # 28 of 30 (Start time: 07:08:57 PM, 28 June 2020)

Total Mark

When two or more product terms are summed by Boolean addition, the result is a _____ expression.

Select the correct option



POS



SOP



Boolean



Simplified

All Answer Are Not Correct (Solve itself)

Question # 29 of 30 (Start time: 07:09:45 PM, 28 June 2020)

Total Mark

The output of an AND gate is one when _____

Select the correct option



All of the inputs are one



Any of the input is one



Any of the input is zero



All the inputs are zero

All Answer Are Not Correct (Solve itself)

Question # 30 of 30 (Start time: 07:10:24 PM, 28 June 2020)

Total Mark

In the expression $F = A \oplus B$, the $\oplus$ represents _____ logical operator.

Select the correct option

Reload Math Equation



XOR



OR



AND



NAND

All Answer Are Not Correct (Solve itself)